

IN THE CLAIMS:

Please amend the claims to read as follows:

Claim 1 (Currently Amended): A plasma display, said display comprising:

a plasma display module;

an interface board controlled by a control voltage and supplied with a source voltage so as to produce an image signal and an inside voltage source control signal; and

an external power circuit which supplies said source voltage to said interface board in accordance with an auxiliary voltage,

~~wherein source voltages of said interface board are supplied from said external power circuit, and~~

wherein said plasma display module ~~further comprises~~ includes:

a plasma display panel;

~~driving circuits which drive~~ a driving circuit for driving said plasma display panel in response to driving voltages supplied thereto thereby causing said plasma display to perform its display operation on the basis of said image signal; and

an inside [[a]] power circuit ~~into which driven by~~ an external alternating current is inputted, ~~said power circuit supplying driving voltages to said driving circuits, and outputting an external source voltage to be used by said external power circuit and , for producing said [[a]] control voltage for controlling operations of said interface board and said auxiliary source voltage, operations of said inside power circuit being controlled to supply said driving voltages to said driving circuit in accordance with said inside voltage source control~~ signal signals output by supplied from said interface board.

Claim 2 (Currently Amended): A plasma display ~~module~~ according to claim 1, wherein said inside voltage source control ~~signals output by said interface board~~ signal ~~contain~~ contains first and second source control signals, and said inside power circuit ~~outputs~~ supplies said ^{*}control voltage to said interface board when said external alternating current is inputted to said inside power circuit, and ~~outputs~~ supplies said ~~external power~~ auxiliary voltage to said external power circuit ~~when~~ in response to said first source control signal ~~is inputted to said power circuit~~, and ~~outputs~~ supplies said driving voltages to said driving ~~circuits~~ ~~when~~ circuit in response to said second source control signal ~~is inputted to said power circuit~~.

Claim 3 (Currently Amended): A plasma display ~~module~~ according to claim 1, wherein said driving voltages ~~contain a plurality of voltages having~~ have different values from each other, and said inside power circuit starts up a lowest-value voltage of said ~~plurality of~~ driving voltages at a timing earlier than that of a highest-value voltage of said ~~plurality of~~ driving voltages, in response to said inside voltage source control signal.

Claim 4 (Currently Amended): A plasma display ~~module~~ according to claim [[1]] 3, wherein ~~said driving voltages contain a plurality of voltages having different values,~~ and said inside power circuit shuts down [[a]] said highest-value voltage at a timing of ~~said plurality of voltages~~ earlier than that of [[a]] said lowest-value voltage, in response to said inside voltage source control signal of said plurality of voltages.

Claims 5-8 (Canceled).

Claim 9 (Previously Presented): A plasma display module comprises:

a plasma display panel;

driving circuits which drive said plasma display panel; and

a power circuit into which an external alternating current is inputted from outward, said power circuit supplying driving voltages to said driving circuits, and outputting an external source voltage to be used by an external power circuit and a control voltage for controlling operations of an interface board to which source voltages of said interface board are supplied from said external power circuit, and operations of said power circuit being controlled with control signals output by said interface board, wherein said control signals output by said interface board contain first and second control signals, and said power circuit outputs said control voltage to said interface board when said external alternating current is inputted to said power circuit, outputs said external power voltage to said external power circuit when said first control signal is inputted to said power circuit, and outputs said driving voltages to said driving circuits when said second control signal is inputted to said power circuit.

Claim 10 (Previously Presented): A plasma display module according to claim 9, wherein said driving voltages contain a plurality of voltages having different values, and said power circuit starts up a lowest-value voltage of said plurality of voltages earlier than a highest-value voltage of said plurality of voltages.

Claim 11 (Previously Presented): A plasma display module according to claim 9, wherein said driving voltages contain a plurality of voltages having different values, and said power circuit shuts down a highest-value voltage of said plurality of voltages earlier than a lowest-value voltage of said plurality of voltages.

Claim 12 (Previously Presented): A plasma display module according to claim 9, wherein said driving voltages contain a plurality of voltages having different values, and said power circuit starts up a lowest-value voltage of said plurality of voltages earlier than a highest-value voltage of said plurality of voltages and shuts down said highest-value voltage earlier than said lowest-value voltage.

Claim 13 (Previously Presented): A plasma display module according to claim 9, wherein said driving voltages contain a plurality of voltages having different values, and said power circuit starts up a lowest-value voltage of said plurality of voltages earlier than a highest-value voltage of said plurality of voltages.

Claim 14 (Previously Presented): A plasma display module according to claim 9, wherein said driving voltages contain a plurality of voltages having different values, and said power circuit shuts down a highest-value voltage of said plurality of voltages earlier than a lowest-value voltage of said plurality of voltages.

Claim 15 (Previously Presented): A plasma display module according to claim 9, wherein said driving voltages contain a plurality of voltages having different values, and said power circuit starts up a lowest-value voltage of said plurality of voltages earlier than a highest-value voltage of said plurality of voltages and shuts down said highest-value voltage earlier than said lowest-value voltage.

Claim 16 (New): A plasma display according to claim 1, wherein said inside power circuit initiates its production of said control voltage earlier than said auxiliary voltage.

Claim 17 (New): A plasma display module, comprising:

a plasma display panel;

a driving circuit for driving said plasma display panel in response to driving voltages supplied thereto thereby causing said plasma display to perform its display operation on the basis of an image signal applied thereto; and

an inside power circuit driven by an external alternating current, and producing a control voltage for controlling operations of an interface board which is operative under a source voltage supplied thereto from an external power circuit and an auxiliary voltage for causing said external power circuit to be active,

said inside power circuit being controlled for supplying said driving voltages to said driving circuit, in accordance with a voltage source control signal supplied from said interface board.

Claim 18 (New): A plasma display module according to claim 17, wherein said inside voltage source circuit initiates its production of said control voltage earlier than said auxiliary voltage.

Claim 19 (New): A plasma display module according to claim 16, in which said inside power circuit starts up a lowest-value voltage of said driving voltages at a timing earlier than that of a highest-value voltage of said driving voltages, in response to said inside voltage source control signal.

Claim 20 (New): A plasma display module according to claim 17, wherein said inside power circuit shuts down said highest-value voltage at a timing earlier than that of said lowest-value voltage, in response to said voltage source control signal.